

## **Ferroelectric Hafnium Oxide and its Applications in Non-Volatile Memories, Negative Capacitance Elements, and Neuromorphic Networks**

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In the last 10 years, the interest in HfO<sub>2</sub> or ZrO<sub>2</sub> based ferroelectric films for novel semiconductor applications steadily increased. Lead-free CMOS compatible ferroelectric layers even below 10 nm film thickness enable scalable devices like high aspect ratio ferroelectric capacitors (FeCap) and field-effect transistors (FeFET) in 2x nm technology nodes [1][2]. But also further applications like ferroelectric tunnel junctions, negative capacitance FETs (NCFET), neuromorphic, piezo, and pyro electric devices are in discussion [3],[4]. Ferroelectric properties are caused by a polar orthorhombic Pca<sub>21</sub> structure in polycrystalline films with a grain size of typically 20-30 nm. TEM and PFM studies are revealing multiple phases within one grain in the pristine case leading to larger (> 100 nm) domains after field cycling [5][6]. By placing a doped HfO<sub>2</sub> layer with a small number of grains within the gate-stack of a nanoscale FeFET structure having the channel length of 30 nm, a detailed analysis of the switching behavior of these single grains is possible [7]. Nucleation limited switching is observed with slightly different coercive fields for different single grains. Multiple excitation pulses, each of which is insufficient for polarization reversal, induce an accumulative effect, which eventually leads to ferroelectric switching [8]. In addition, such an accumulative switching can be exploited to mimic the integrate-and-fire activity of biological neurons [9], which, together with FeFET-based synapses [10], might allow for building fundamental computing blocks of brain-inspired neural networks. Recently, it is shown that ferroelectrics can possess a negative capacitance [4], which could improve the energy-efficiency of conventional CMOS transistors beyond fundamental limits. For this purpose, a ferroelectric HfO<sub>2</sub> layer is incorporated into a capacitor together with a dielectric layer to prevent the initial screening of polarization charges during switching. The results are giving an inside view on the fundamental physics of ferroelectricity which indicate the energy barrier in a doped HfO<sub>2</sub> double well potential as the origin of negative capacitance [11]. Here, the fast and hysteresis-free ferroelectric switching behavior of a negative capacitance device can be confirmed, which is critical for future applications.

- [1] T. S. Böске, J. Müller, D. Bräuhäus, U. Schroeder, U. Böttger, *Appl. Phys. Lett.*, 99, 10, 102903 (2011).
- [2] T. S. Böске, J. Müller, D. Bräuhäus, U. Schroeder, U. Böttger, *IEEE Symposium IEDM 2011*
- [3] S. Fujii, Y. Kamimuta, T. Ino, Y. Nakasaki, R. Takaishi, and M. Saitoh, *IEEE Symposium on VLSI Technology* (2016) 148.
- [4] S. Salahuddin, S. Datta, *Nano Lett.* 8, 405–410 (2008)
- [5] E. Grimley, T. Schenk, T. Mikolajick, U. Schroeder, and J. LeBeau, *Adv. Mater. Interfaces* 1701258 (2018)
- [6] I. Stolichnov, M. Cavaliere, E. Colla, T. Schenk, T. Mittmann, T. Mikolajick, U. Schroeder, and A. Ionescu, *ACS Appl. Mater. Interfaces* (2018),
- [7] H. Mulaosmanovic, J. Ocker, S. Müller, U. Schroeder, J. Müller, P. Polakowski, S. Flachowsky, R. Bentum, T. Mikolajick, and S. Slesazek, *ACS Appl. Mater. Interfaces* 9, 3792 (2017)
- [8] H. Mulaosmanovic, T. Mikolajick, and S. Slesazek, *ACS Appl. Mater. Interfaces* 10, 23997 (2018)
- [9] H. Mulaosmanovic, E. Chicca, M. Bertele, T. Mikolajick, and S. Slesazek, *Nanoscale* 10, 21755-21763, (2018).
- [10] Mulaosmanovic, H. et al. *Symposium on VLSI Technology T176–T177* (2017).
- [11] M. Hoffmann, F. Fengler, M. Herzig, T. Mittmann, B. Max, U. Schroeder, R. Negrea, P. Lucian, S. Slesazek, T. Mikolajick, *Nature* 565, 464–467 (2019)